ELECTRICAL REF DESIGNATION	ITEM	PART NUMBER	DESCRIPTION	QTY
C1,2,11,20,95,	1	702W07301	CAP., 20, 15V	8
112,171,181				
C3 THRU 10.	2	702W07818	CAP., 0.1, 50V	181
12 THRU 19,				
21 THRU 94,				
96 THRU 111,				
113 THRU 170,				
172 THRU 180,				
182 THRU 189				
F1	3	708W11402	FUSE, 10 AMP	1
R1 THRU 7,9,	4	703W27888	RES, 15 OHMS	71
13 THRU 19,22.				
24 THRU 31,34,				
36 THRU 42,44,45,				
48 THRU 55,57,				
60 THRU 63,				
65 THRU 69,				
72 THRU 78,81,82,				
85,88 THRU 96				
R8,10,11,20,21,	5	703W27488	RES, 10 OHMS	24
23,32,33,35,43,				
46,47,56,58,59,				
64,70,71,79,80,				
83,84,86,87			·	
R12,113,129,132	6	70 3W32288	RES, 1K OHMS	4
R97 THRU 112,128	7	70 3W28488	RES, 27 OHMS	17
R114,	8	:03P10005	RES, ZERO OHMS	12
117 THRU 124,				
126,130,134				
R136 THRU 145	9	3 W30688	RES, 220 OHMS	10
U1 THRU 4,	10	3W01562	IC, MK4164	_ 68
9 THRU 12,		1		
17 THRU 20,		1		
25 THRU 28,		1		
34 THRU 37,		i		

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PWB ASSY, MCC-P (256K)	DWG.	DWG.	SHEET
	SIZE	NO. 140S05950	REV
29/2	A4	SHEET 1 - OF 3	Α

^		· I		
ELECTRICAL REF DESIGNATION	ITEM	PART NUMBER	DESCRIPTION	. 014
42 THRU 45,				
51 THRU 54,				
60 THRU 63,				
68 THRU 71,				
77 THRU 80,				
86 THRU 89,				
94 THRU 97,				
103 THRU 106,				
111 THRU 114,				
120 THRU 123,				
129 THRU 132,				
137 THRU 140				
U33,85,102	11	703W13291	IC, 1KX15	3
U50,59,76,119,	12	733W01634	IC, SN74S241	9
128,145,165,211,				
212				
U146 THRU 154,	13	733W01633	IC. SN74S240	16
156,158,164,182,				
200,202,207				
U155,157	14	733W02136	IC, SN74S37	2
U159,166,179,203	15	733W01638	IC, SN74S280	4
U172,183,192	16	733W01616	IC, SN74S138	3
U161	17	733W01635	IC, SN74S251	1
U162,163,171,177,	18	733W00318	IC, SN74S00	6
184,209				
U167,213 ·	19	733W01606	IC, SN74S10	2
U168	20	73 3W01648	IC, SN74S86	1
U169,185,193	21	733W01 771	IC, SN74S74	3
U170,194,201	22	73 3W01643	IC, SN74S02	3
U174	23	733W01663	IC, SN74LS393	1
U175,208	24	73 3W01640	IC, SN74S374	2
U176,210	25	733W00319	IC, SN74S04	2
U178,186	26	744W00001	IC, DELAY LINE	2
U180,181,188,189,	27	733W01636	IC, AM74S253	8
196,197,204,205				

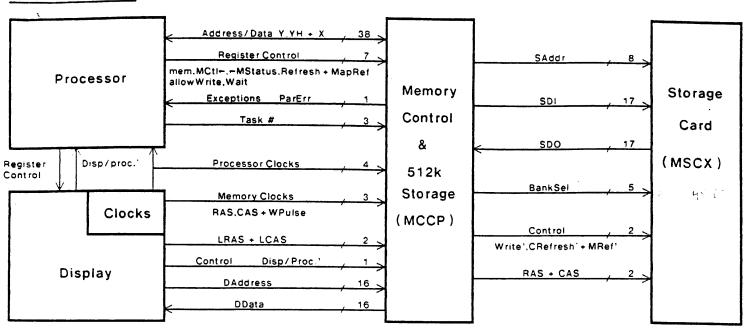
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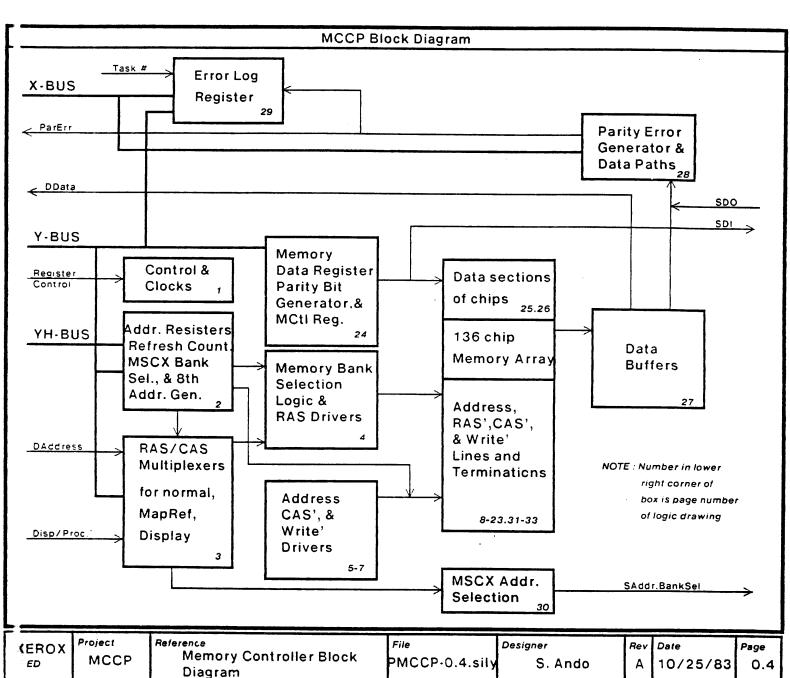
TITLE	B ASSY, MCC-P (256K)	DWG.	DWG.	14	050	5950		SHEE
	,	A4	SHEE	т .	2 -	OF	3	A

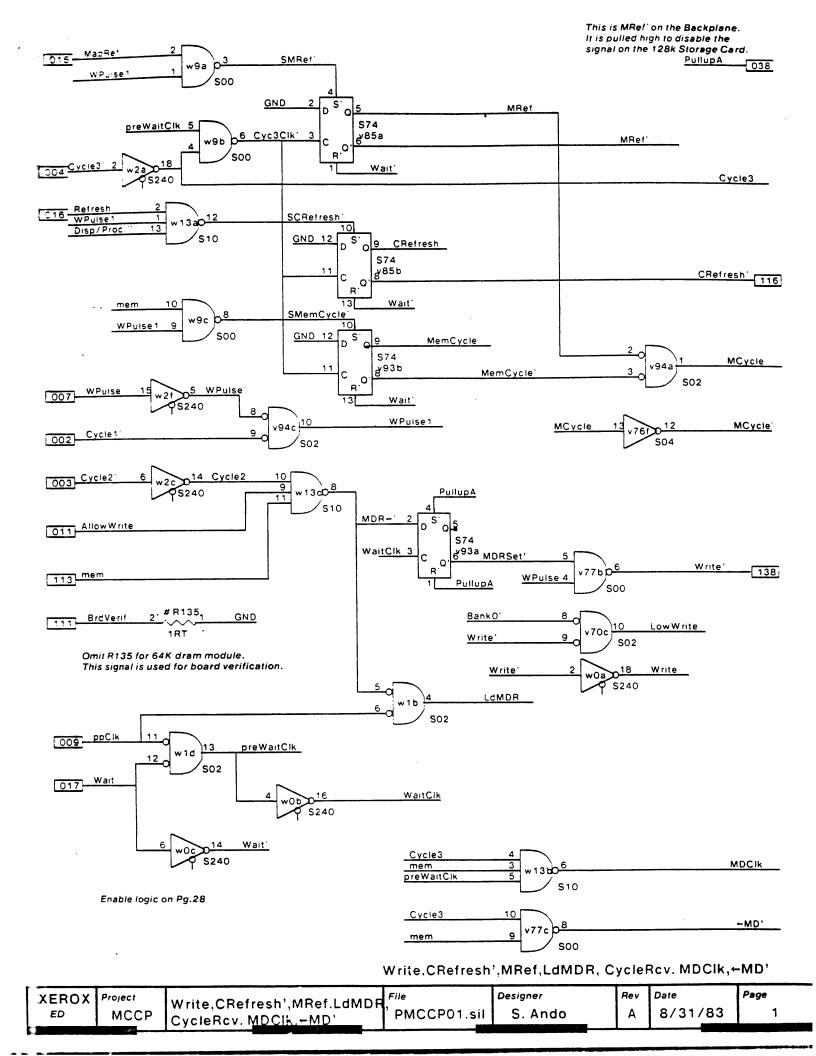
TABLE OF CONTENTS

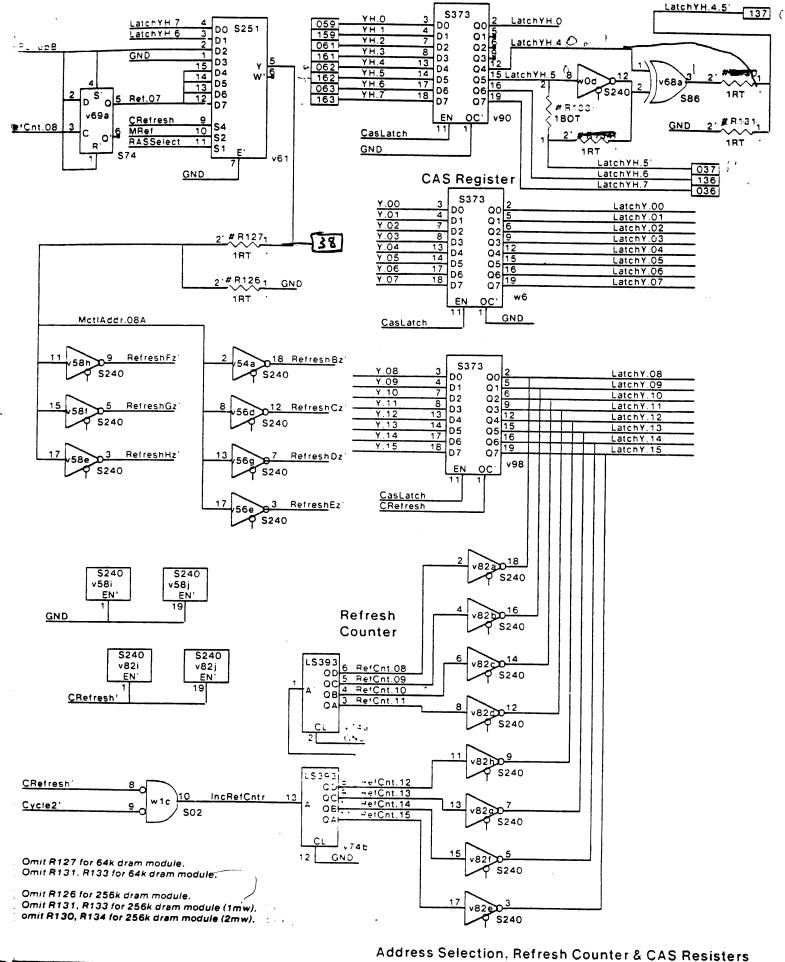
	SHEET	
	0.4	MEMORY CONTROLLER BLOCK DIAGRAM
	1.	WRITE, CREFRESH', MREF, LDMDR, CYCLE RCV, MDCLK, ←MD
	2.	ADDRESS SELECT., REFRESH COUNT, CAS RESISTERS
	3.	ADDRESS SELECTION LOGIC, RASDLY, LRASDLY
	4.	MEMORY BANK SELECTION
	5.	MEMORY BANK SELECTION, DRIVERS FOR WRITE' & CAS'
	6.	ADDRESS DRIVERS
	7.	ADDRESS DRIVERS
	8.	ADDRESS, RAS, CAS, WRITE DISPLAY BANK
	9.	ADDRESS, RAS, CAS, WRITE DISPLAY BANK
	10.	MEMORY CONTROL/ BANK B
	11.	MEMORY CONTROL/ BANK B
	12.	MEMORY CONTROL/ BANK C
	13.	MEMORY CONTROL/ BANK C
	14.	MEMORY CONTROL/ BANK D
	15.	MEMORY CONTROL/ BANK D
	16.	MEMORY CONTROL/ BANK E
	17.	MEMORY CONTROL/ BANK E
	18.	MEMORY CONTROL/ BANK F
	19.	MEMORY CONTROL/ BANK F
	20.	MEMORY CONTROL/ BANK G
	21.	MEMORY CONTROL/ BANK G
	22.	MEMORY CONTROL/ BANK H
	23.	MEMORY CONTROL/ BANK H
	24.	MEM. DATA REG., MEM. CTL. REG., PARITY BIT GENERATOR
	25.	WORK STATION MEMORY
	26.	WORK STATION MEMORY
	27	DATA BUFFERS
	28	PARITY ERROR, DATA PATHS
	29	ERROR LOG REGISTER
	30	MSC ADDRESS SELECTION
	31	DISCRETE RESISTORS
	32	DISCRETE RESISTORS
	33	RESISTOR DIPS, TERMINATIONS
-,	34	POWER PINS - FUSES - CAPS
· · .	35	DISCRETE CAPACITORS
	36	DISCRETE CAPACITORS
	37	DISCRETE CAPACITORS
** ***	·.	

V # D G V	Project	Reference	File				
XEROX	MCCD			_	Rev	Date	Page
€D	MCCP	Table of Contents	PMCCP-0.3.sil	y S. Ando	Α	10/25/83	0.3
L							0.0

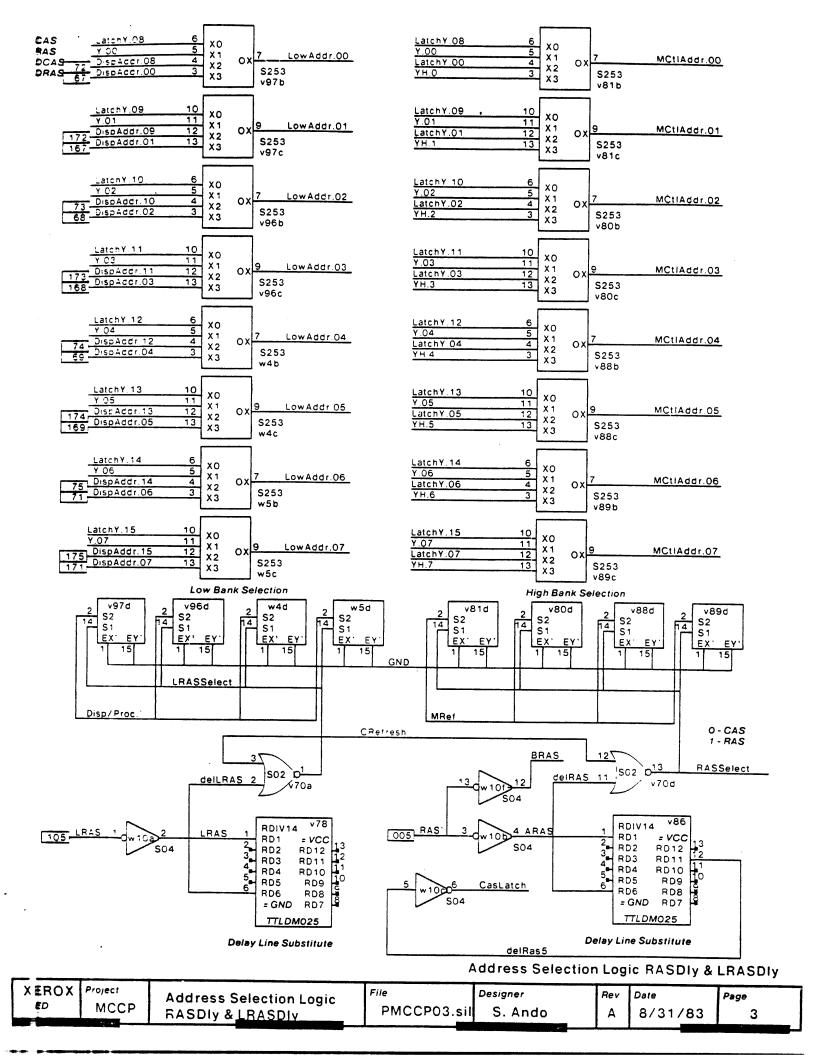


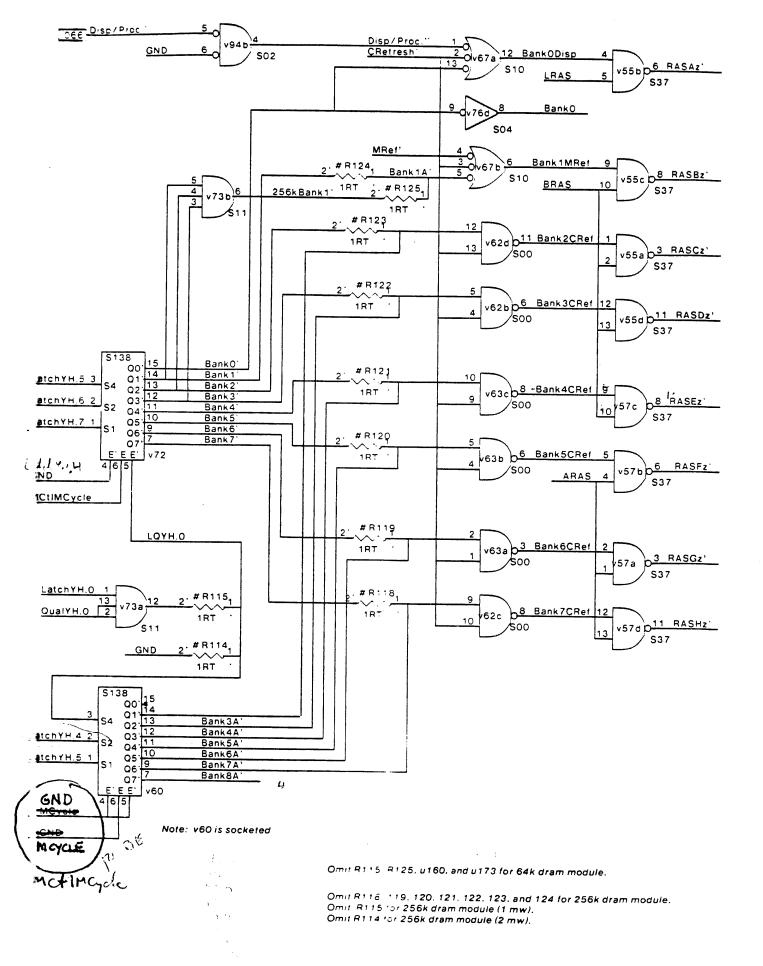




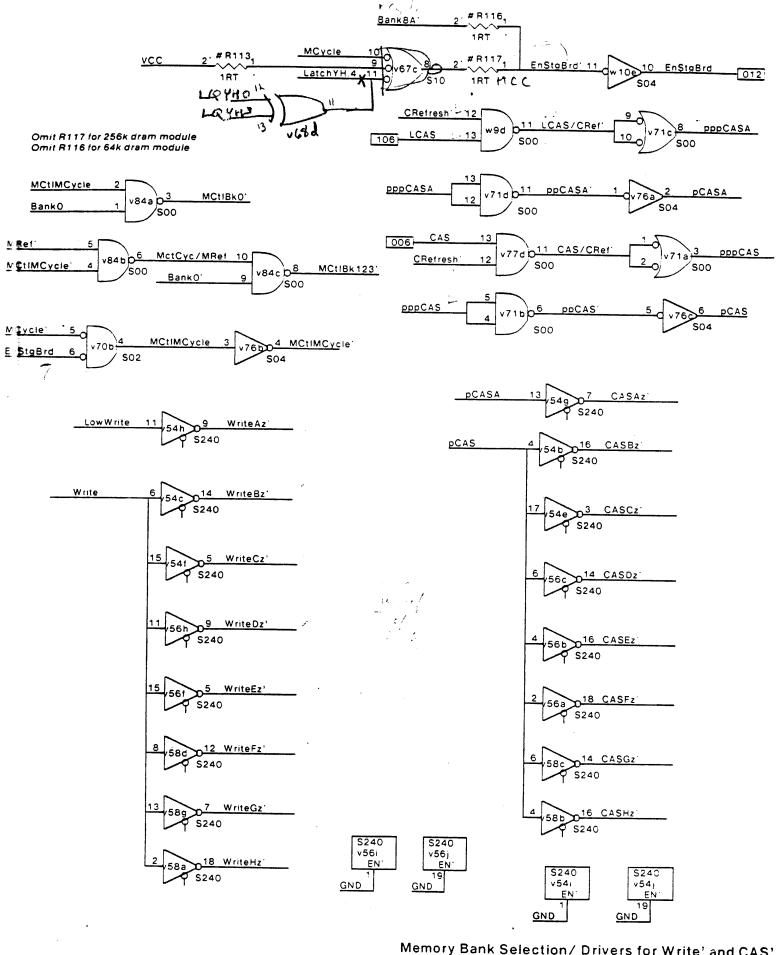


KEROX Project Addr. Select., Refresh Count. File Designer Rev Date Page
MCCP & CAS Resisters PMCCP02.sil S. Ando A 8/31/83 2



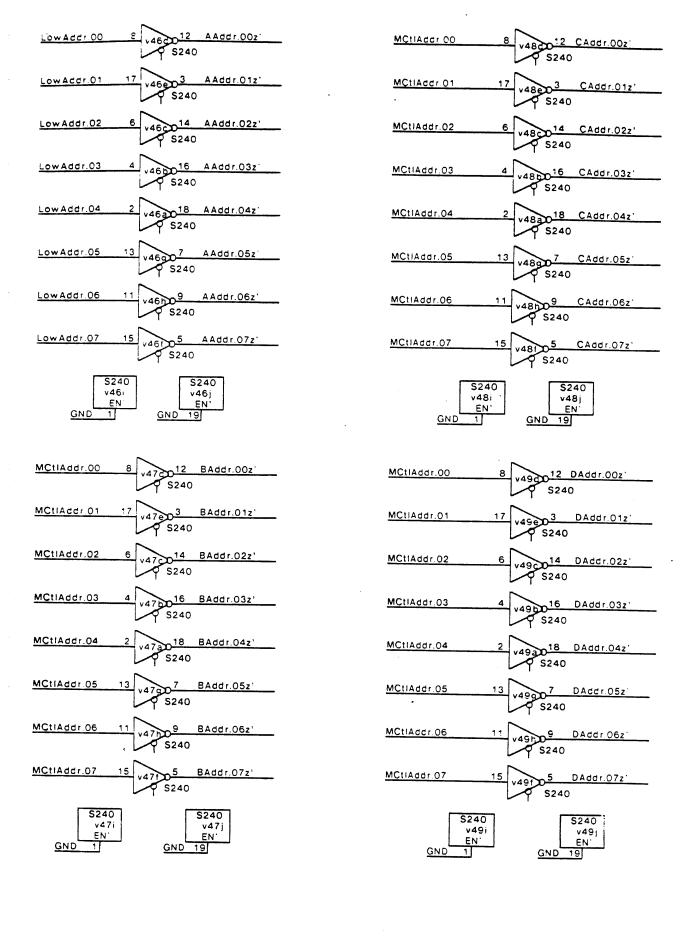


XEROX Project . File Designer Rev Date Page
MCCP Memory Bank Selection PMCCP04.sil S. Ando A 8/31/83 4



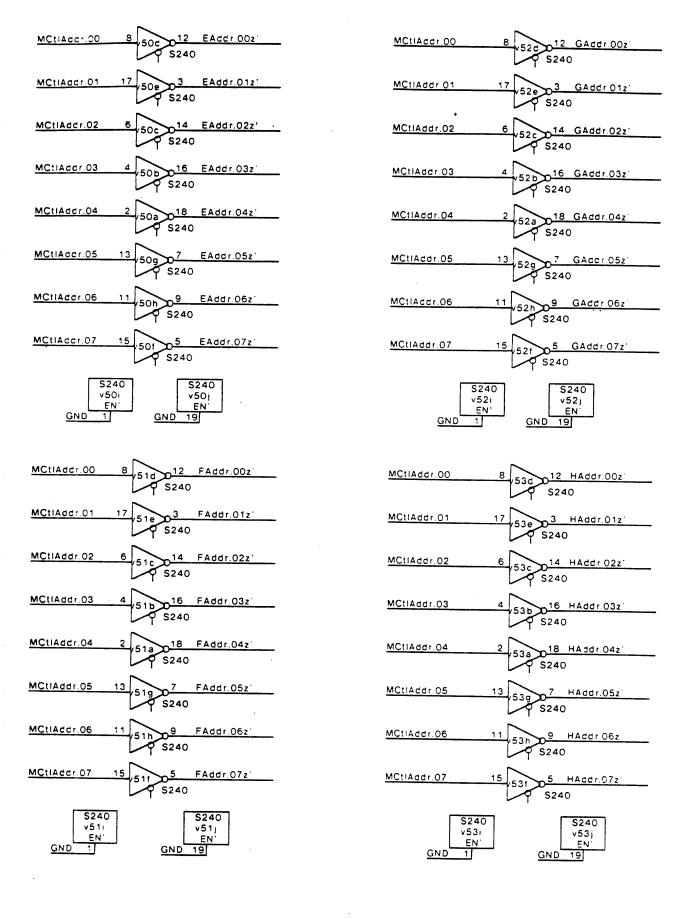
Memory Bank Selection / Drivers for Write' and CAS'

Į	₹EROX	Project	Memory Bank Selection	File	Designer	Rev	Date	Page
	ED	MCCP	Drivers for Write' & CAS'	PMCCP05.sil	S. Ando	Δ	10/17/83	
1						L		_



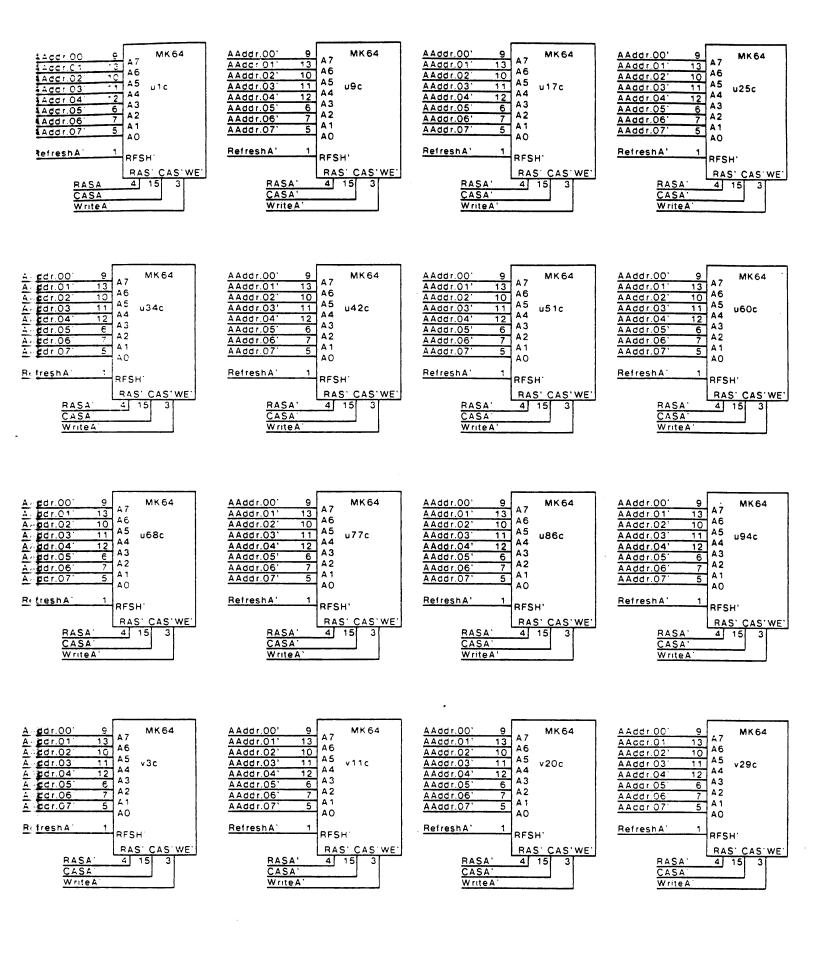
Address Driver	S
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¥500×							_
XEROX	Ĭ	Address Drivers	File	Designer	Rev	Date	Page
- ED	МССР	Address Dilvers	PMCCP06.sil	S. Ando	Α	8/31/83	6
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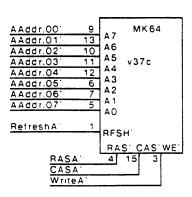
Address Drivers

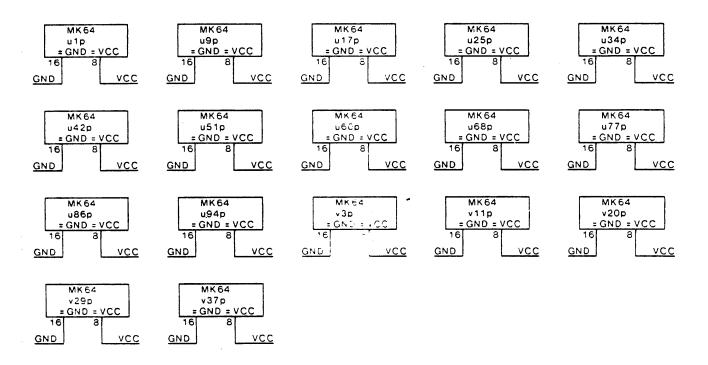
1 5 5 6 W				T			
> EROX	Project		File	Designer	Rev	Date	Page
€D	MCCP	Address Drivers	D1400000			1	rage
	III CCI		PMCCP07.sil	S. Ando	Α	8/31/83	7
						7	



Address, RAS, CAS, Write Display Bank

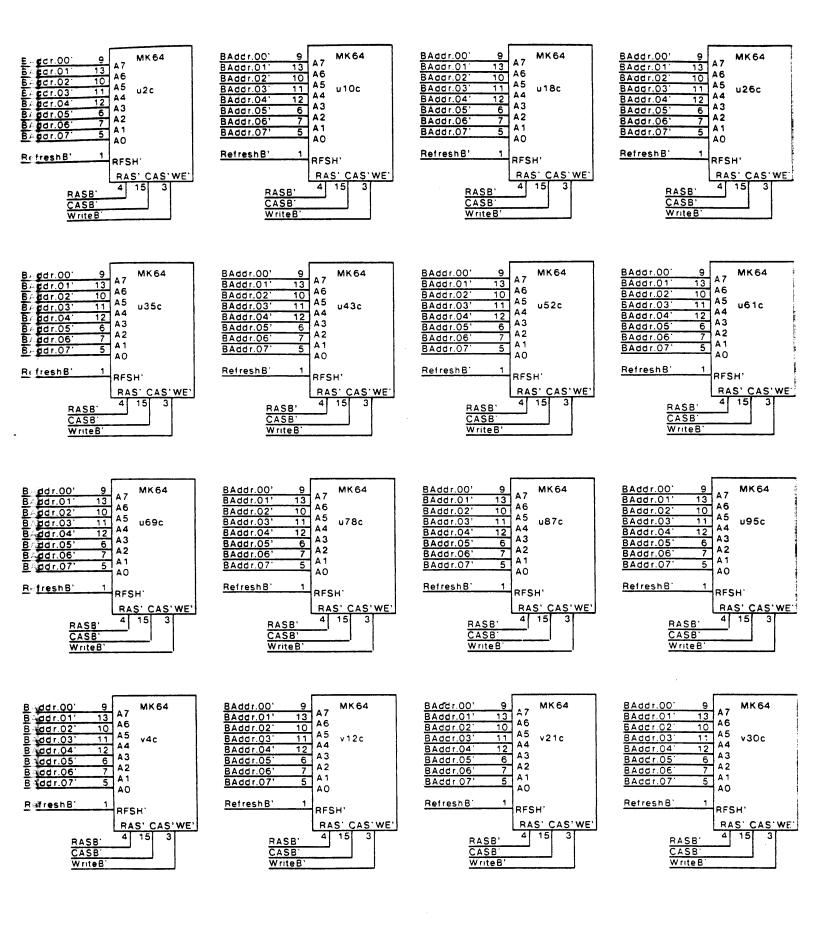
	XEROX	Project	Address.RAS,CAS,Write	File	Designer	Rev	Date	Page
L	ED	МССР	Display Bank	PMCCP08.sil	S. Ando	Α	8/31/83	8





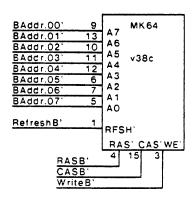
Address.RAS,CAS,Write Display Bank

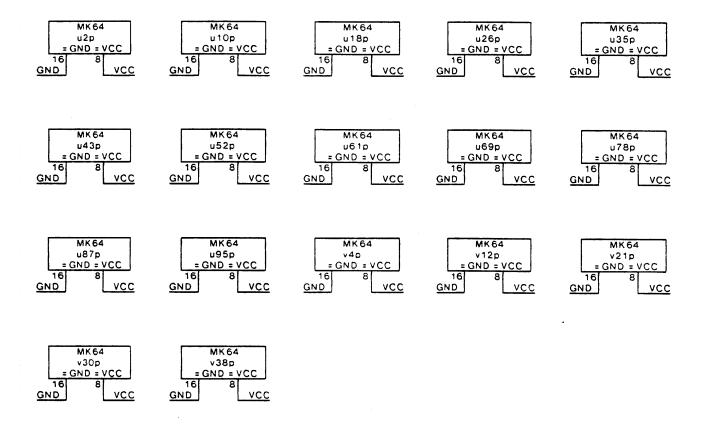
EROX	Project	Address,RAS,CAS.Write	File	Designer	Rev	Date	Page
ED	MCCP	Display Bank	PMCCP09.sil	S. Ando	Α	8/31/83	9



Mem. Control/Bank B

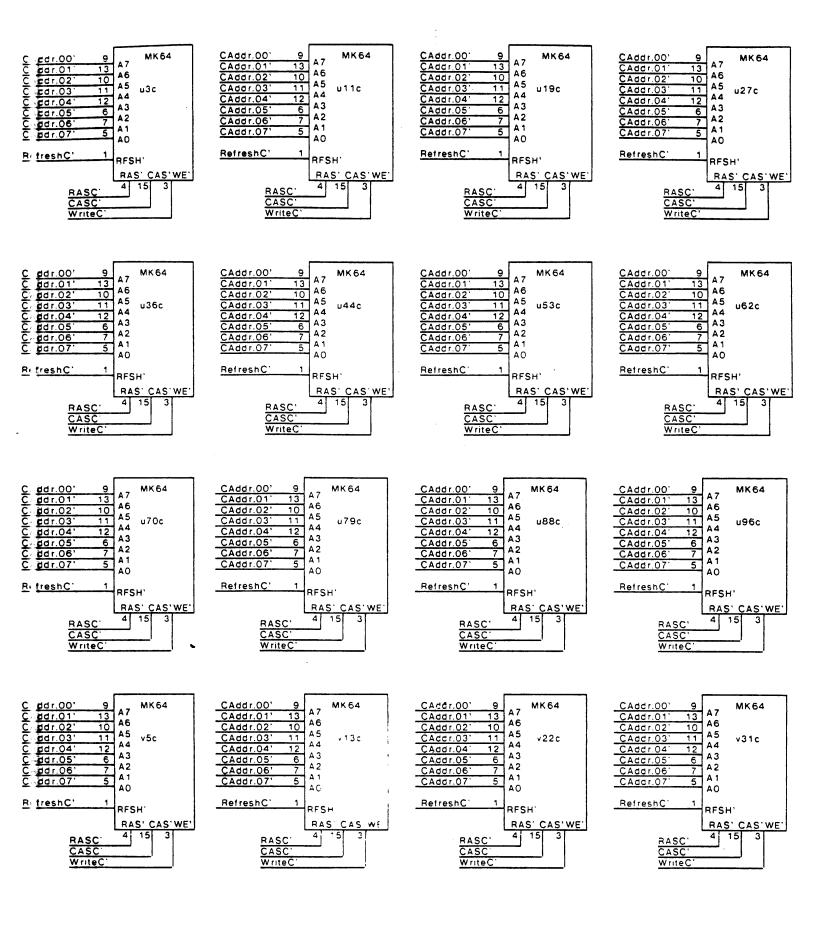
XEROX	Project	•	File	Designer	Rev	Date	Page
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350					L		





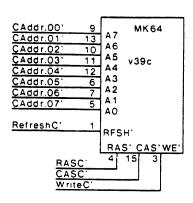
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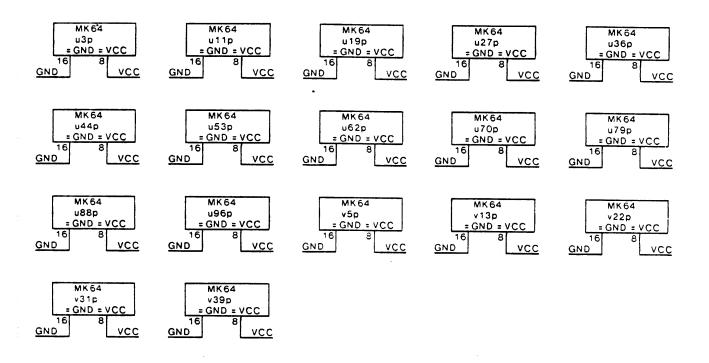
≰EROX	Project		File	Designer	Rev	Date	Page
ED	MCCP	Mem. Control/Bank B	PMCCP11.sil	S. Ando	Α	8/31/83	11
200							



Mem. Control/Bank C

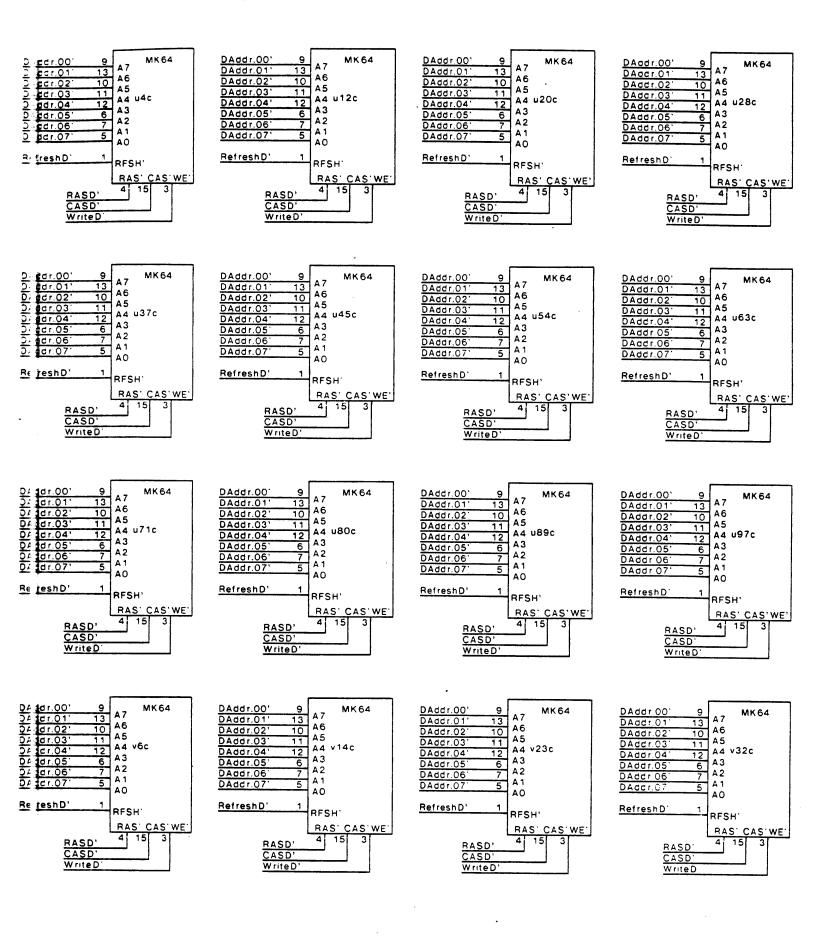
EP	KEROX	Project	•	File	Designer	Rev	Date	Page
	ED	МССР	Mem. Control/Bank C	PMCCP12.sil	S. Ando	Α	8/31/83	12





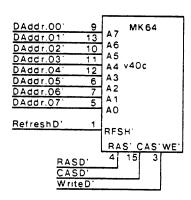
Mem. Control/Bank C

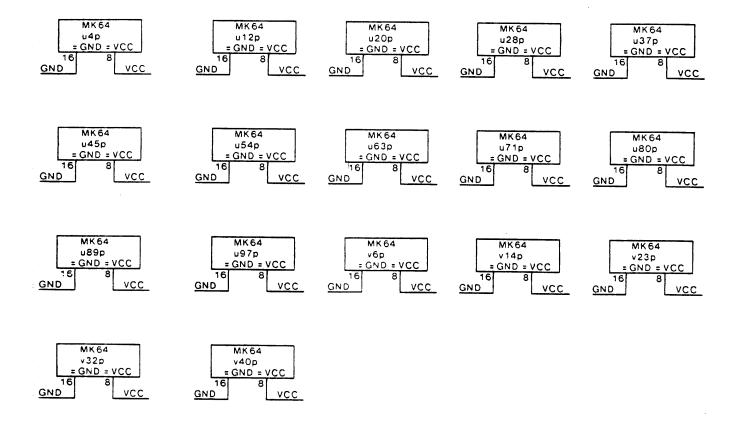
r								
ı	XEROX	Project		File	Designer	Rev	Date	Page
	ED	MCCP	Mem. Control/ Bank C	PMCCP13.sil	S. Ando	Α	8/31/83	13



Mem. Control/Bank D

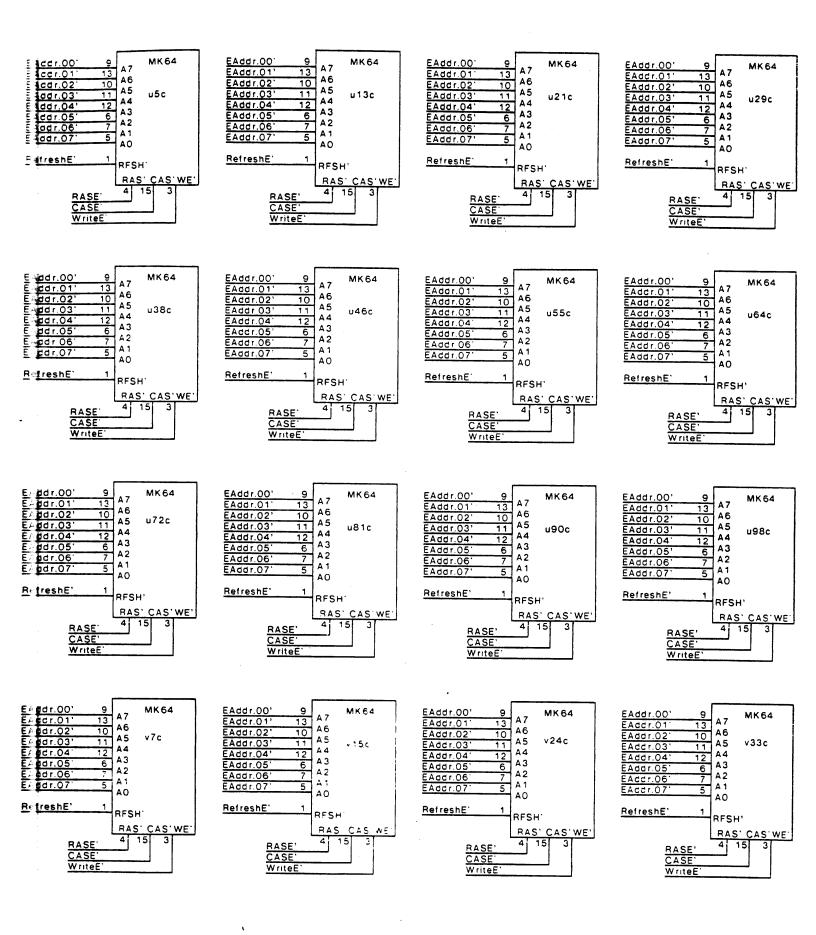
r								
١	∢EROX	Project		File	Designer	Rev	Date	Page
	ED	МССР	Mem. Control/Bank D	PMCCP14.sil	S. Ando	Α	8/31/83	14
1	<u> </u>							





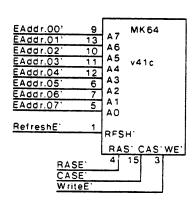
Mem. Control/Bank D

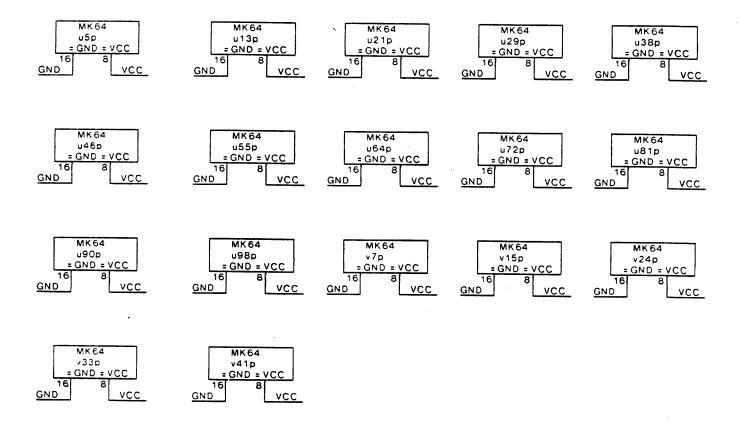
> EROX	Project		File	Designer	Rev	Date	Page
€D	MCCP	Mem. Control/ Bank D	PMCCP15.sil	S. Ando	Α	8/31/83	15
					<u> </u>		



Mem. Control/Bank E

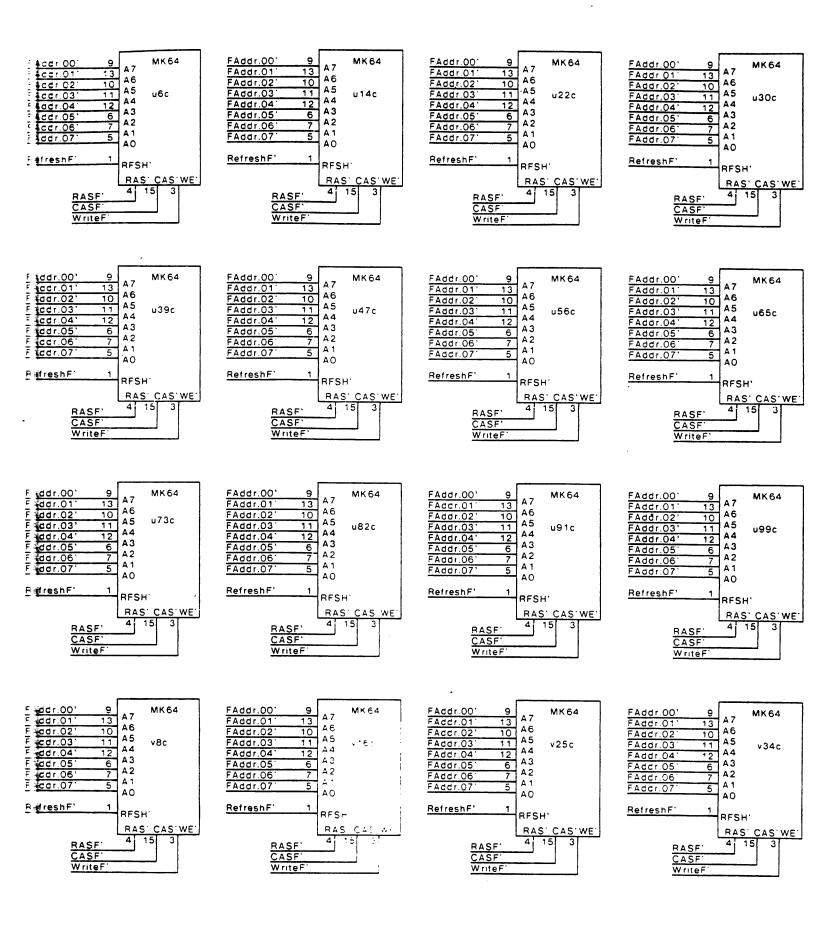
KEROX	Project	·	File	Designer	Rev	Date	Page
ED	MCCP	Mem. Control/Bank E	PMCCP16.sil	S. Ando	Α	8/31/83	16
-					1		





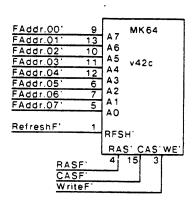
Mem.	Contr	10	Bank E
1410111.		U1/	Dalla

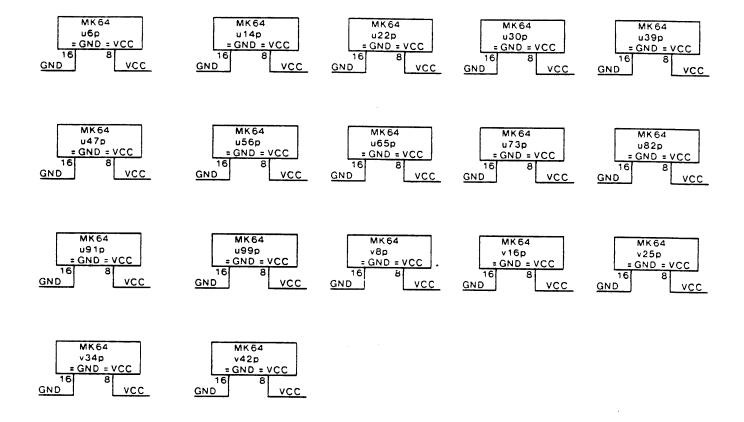
	KEROX	5		File	Designer	Rev	Date	Page
	ED	MCCP	Mem. Control/Bank E	PMCCP17.sil	S. Ando	Α	8/31/83	17
•								



Mem. Control/ Bank F (no dram required for 256k dram module)

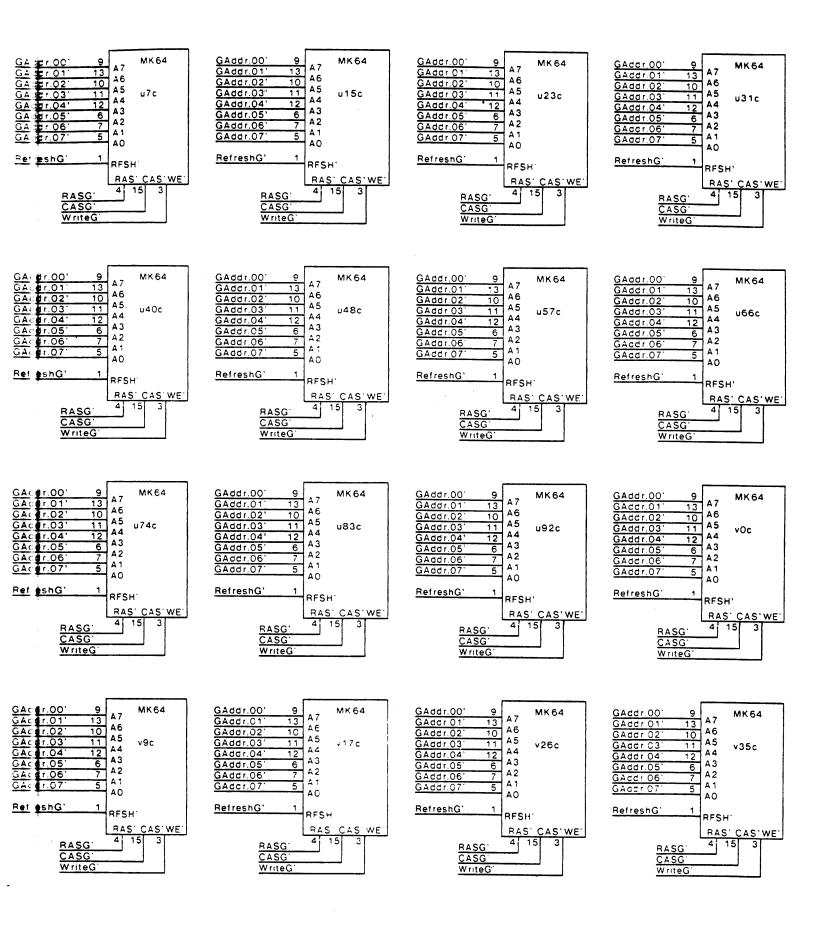
XEROX	Project	•	File	Designer	Rev	Date	Page
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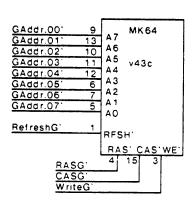
Mem. Control/ Bank F (no dram required for 256k dram module)

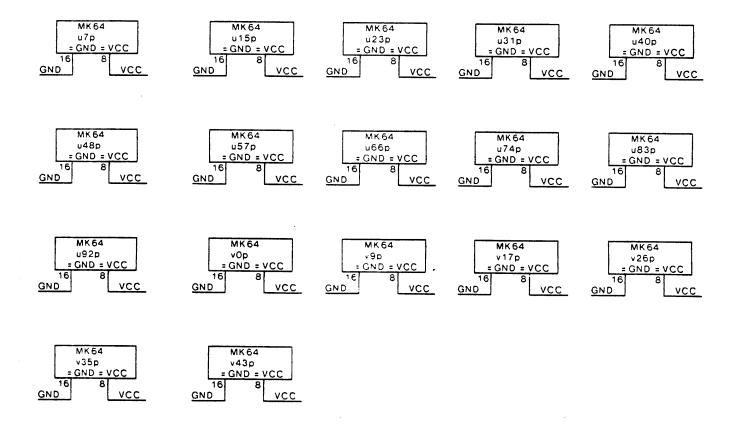
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XEROX	Project	•	File	Designer	Rev	Date	Page
€D	MCCP	Mem. Control/Bank F	PMCCP19.sil	S. Ando	Α	8/31/83	19
Ä							



Mem. Control/Bank G (no dram required for 256k dram module)

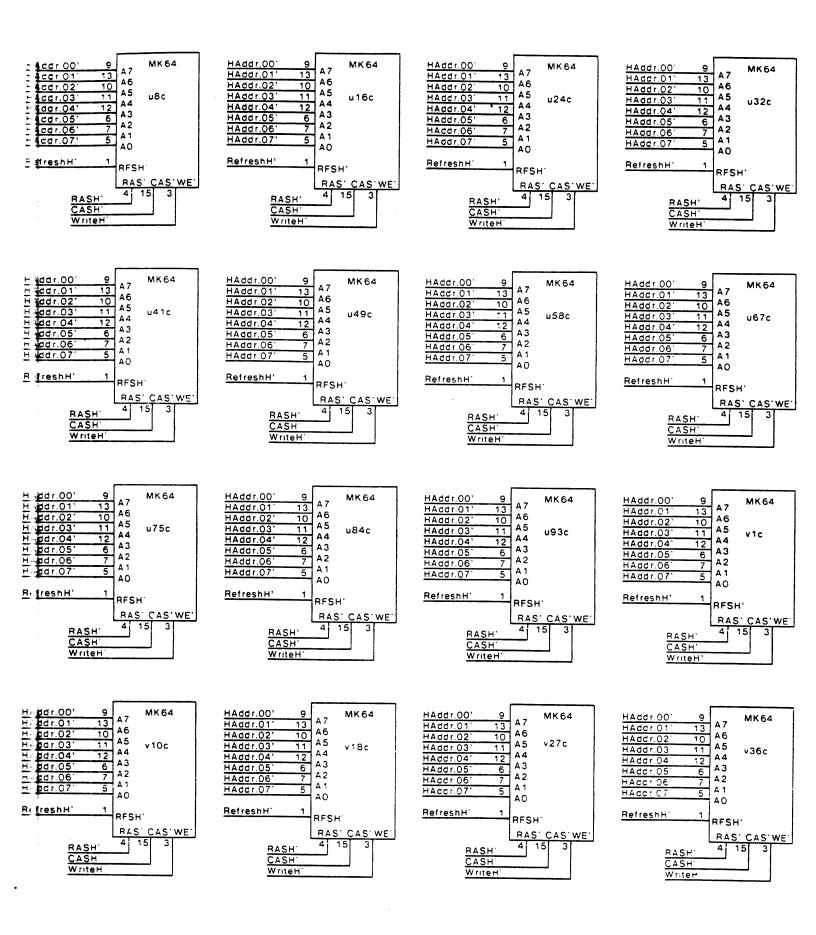
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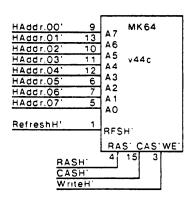
Mem. Control/Bank G (no dram required for 256k dram module)

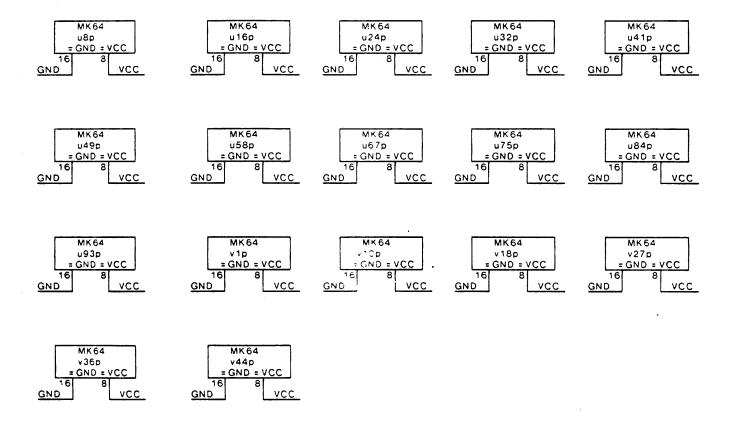
XEROX	1 :	•	File	Designer	Rev	Date	Page
ED	MCCP	Mem. Control/ Bank G	PMCCP21.sil	S. Ando	Α	8/31/83	21



Mem. Control/Bank H (no dram required for 256k dram module)

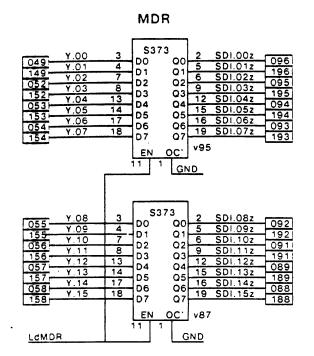
ſ	*EROX	Project		File	Designer	Rev	Date	Page
L	ED	MCCP	Mem. Control/Bank H	PMCCP22.sil	S. Ando	Α	8/31/83	22
	^,					<u> </u>		

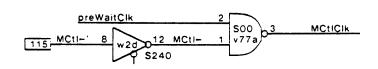




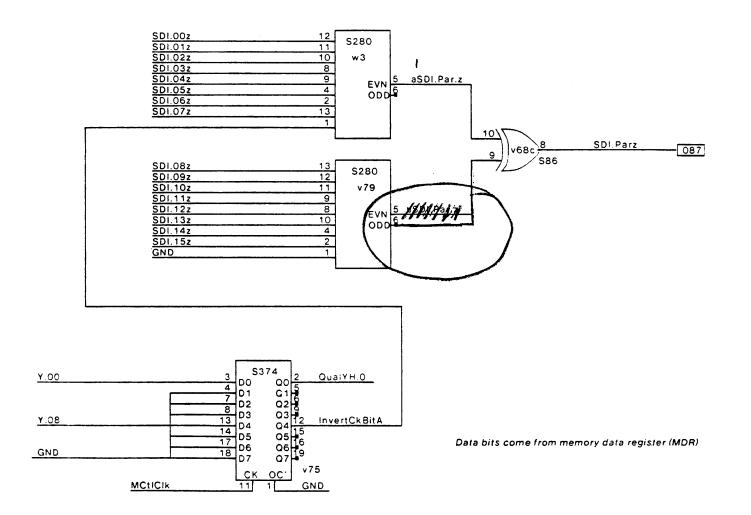
Mem. Controi/ Bank H (no dram required for 256k dram module)

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XEROX	Project	•	File	Designer	Rev	Date	Page
ED	MCCP	Mem. Control/Bank H	PMCCP23.sil	S. Ando	Α	8/31/83	23
*	- Table				<u> </u>	L	





Enable logic on Pg.28



Mem. Data Reg., Mem. Ctl. Reg. & Parity Bit Generator

CEROX	Project	Mem. Data Reg., Mem. Ctl.	File	Designer	Rev	Date	Page
ED	MCCP	Reg. & Parity Bit Generator	PMCCP24.sil	S. Ando	Α	8/31/83	24
5 4 5							

SDI.00 2 MK64 14 MemDisp.00	2 MK64 14 MemData.00	2 MK64 14 MemData.00	2 MK 64 14 MemData.00
\$DI.01 2 MK 64 14 MemDisp.01	2 MK64 14 MemData.01	2 MK 64 14 MemData.01	2 MK 64 14 MemData.01
SDI.02 2 MK 64 14 Mem Disp.02	2 MK64 14 MemData.02	2 MK64 14 MemData.02	2 MK64 14 MemData.02
SDI.03 2 MK64 14 MemDisp.03	2 MK64 14 MemData.03	2 MK64 14 MemData 03	2 MK64 14 MemData 03
\$DI 04 2 MK 64 14 Mem Disp. 04	2 MK64 14 MemData.04	2 MK64 14 MemData.04	2 MK 64 14 Mem Data .04
SDI.05 2 MK64 14 MemDisp.05	2 MK64 14 MemData.05	2 MK 64 14 Mem Data 05	2 MK 64 14 MemData 05
SDI 06 2 MK64 14 MemDisp.06	2 MK64 14 MemData.06	2 MK64 14 MemData.06	2 MK64 14 MemData.06
SDI.07 2 MK64 14 MemDisp.07	2 MK64 14 MemData.07	2 MK64 14 MemData.07	2 MK64 14 MemData.07
\$DI.08 2 MK64 14 MemDisp.08	2 MK 64 14 Mem Data .08	2 MK64 14 MemData.08	2 MK64 14 MemData.08
SDI.09 2 MK64 14 MemDisp.09	2 MK64 14 MemData.09	2 MK64 14 MemData.09	2 MK64 14 MemData.09
SDI.10 2 MK64 14 MemDisp.10	2 MK64 14 MemData.10	2 MK64 14 MemData.10	2 MK 64 14 MemData.10
SDI.11 2 MK64 14 MemDisp.11	2 MK64 14 MemData.11	2 MK64 14 MemData.11	2 MK 64 14 MemData 11
SDI.12 2 MK64 14 MemDisp.12	2 MK64 14 MemData.12	2 MK64 14 MemData.12	2 MK64 14 MemData 12
SDI.13 2 MK64 14 MemDisp.13	2 MK64 14 MemData.13	2 MK64 14 MemData.13	2 MK64 14 MemData.13
V11b SDI.14 2 MK64 14 MemDisp.14	2 MK64 14 MemData.14	2 MK64 14 MemData.14	v14b 2 MK 64 14 MemData 14
SDI.15 2 MK64 14 MemDisp.15	2 MK64 14 MemData.15	2 MK64 14 MemData.15	v23b 2 MK64 14 MemData 15
v29b	v30b	v31b	v32b
SDI Par 2 MK64 14 v37b	2 MK64 14 v38b	2 MK 64 14 v39b	2 MK 64 14 v40b
MemDisp.16	L	Work Station	<u>j spo.p</u> Memory

KEROX Project

ED MCCP

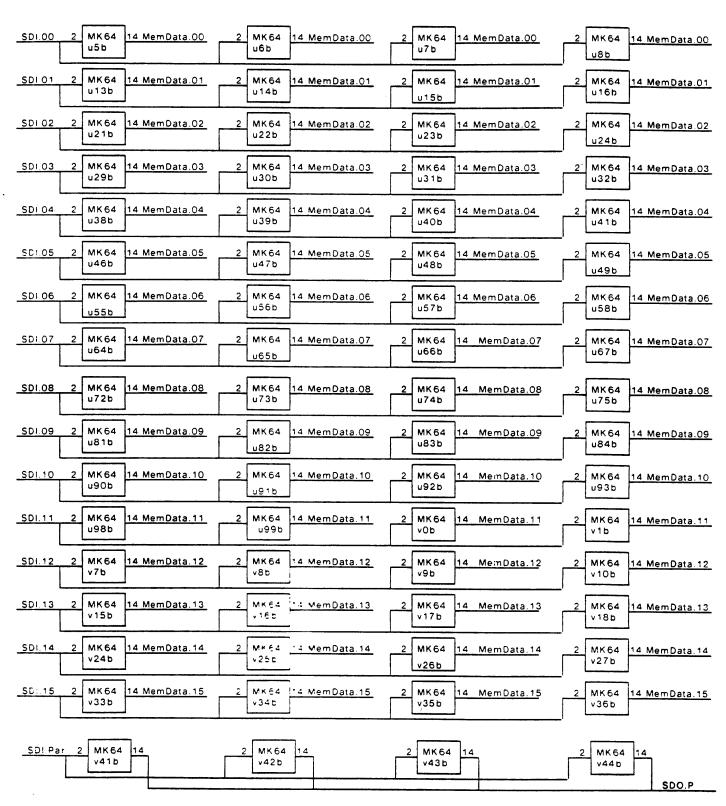
Work Station Memory

File PMCCP25.sil

Designer S. Ando
 Rev
 Date
 Page

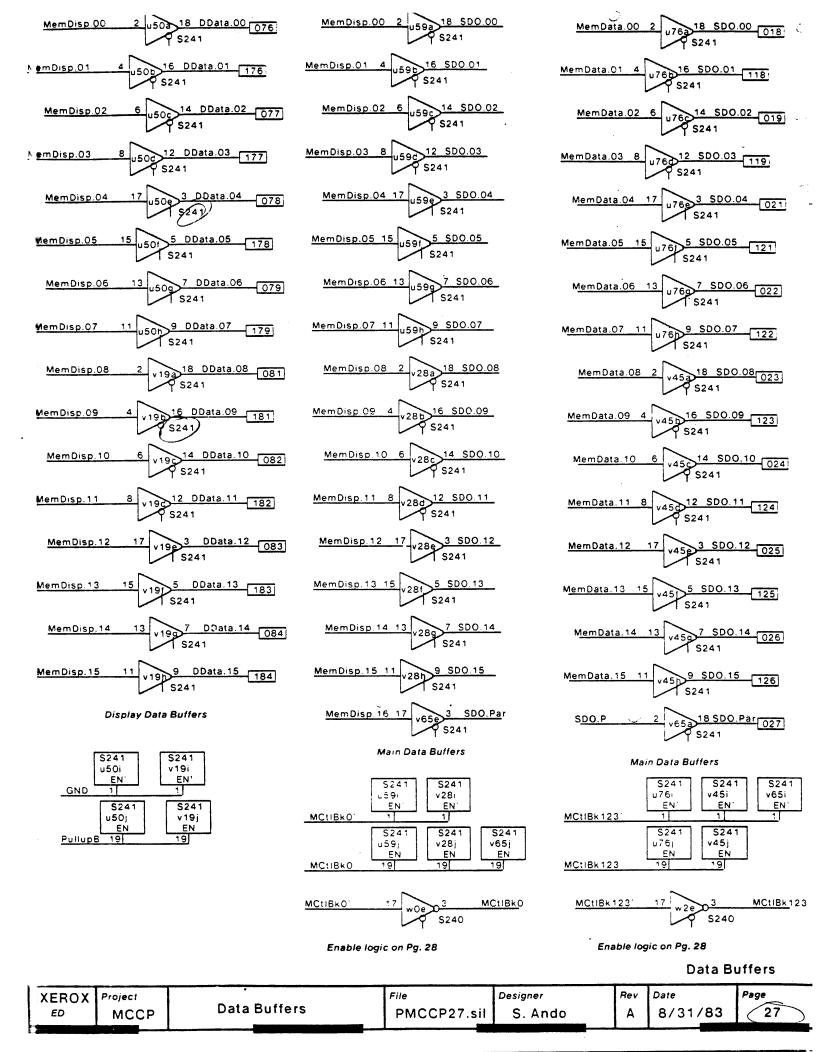
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 8/31/83
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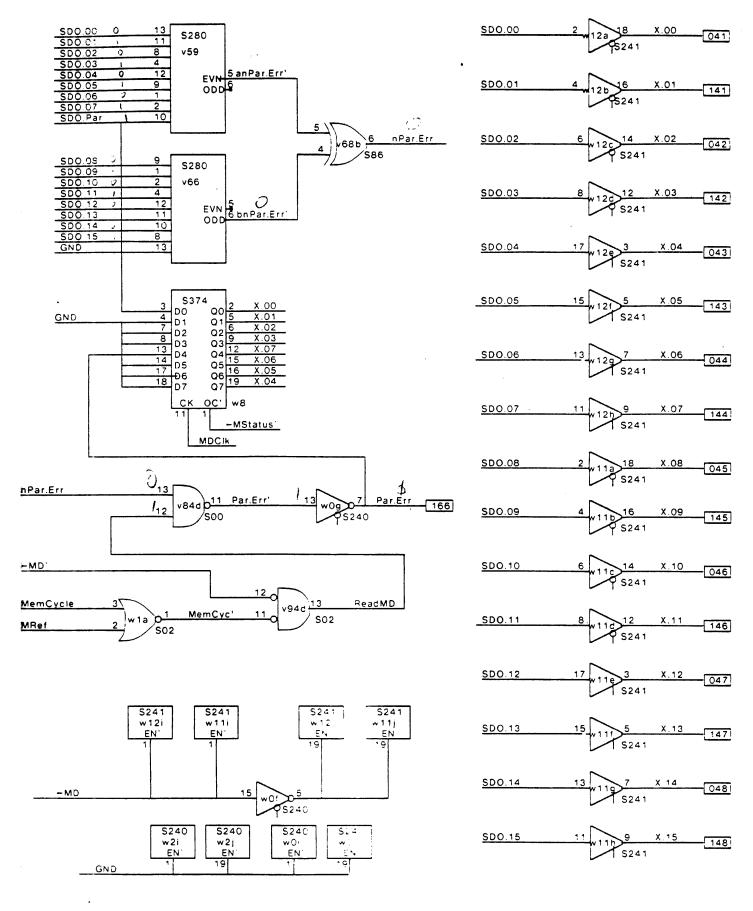
25



Work Station Memory

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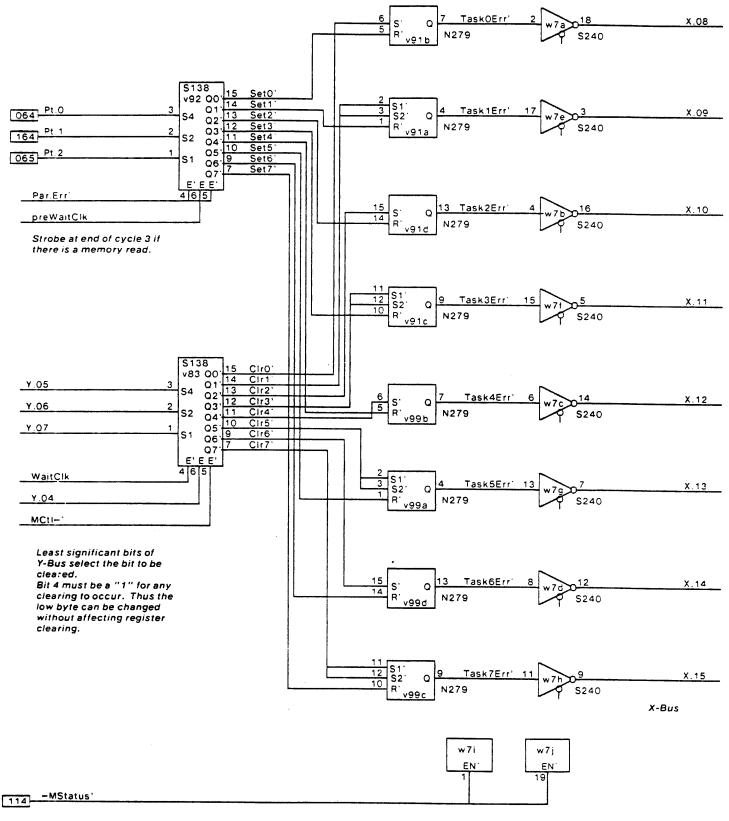




Parity Error & Data Paths

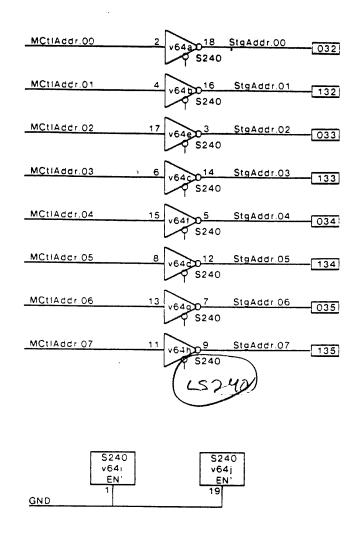
XEROX	Project		File	Designer	Rev	Date	Page
ED	MCCP	Parity.Error & Data Paths	PMCCP28.sil	S. Ando	Α	8/31/83	28
2 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3					Ь		

Errors Register Bit is 1 if there was an error.



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KEROX	Project	•	File	Designer	Rev	Date	Page
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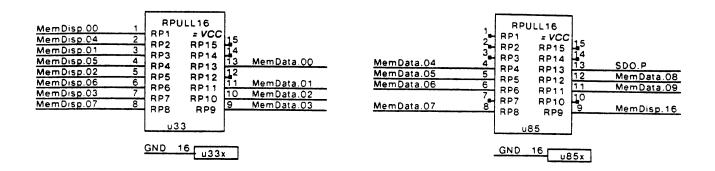
			_CASAz	2· #R11	CASA'	CASC-:	2. #R35 ₁	
501.00z	2. #R97 ₁	SD1.00		1RT .		CASCz	1RT	CASC.
	1RT ' 2. #R105		Write Az '	2· #R10 ₁	Write A.	WriteCz ⁻	2. #R33 ₁	WriteC'
_ <u>SDI.01z</u>	1RT	<u> 501.01</u>		1RT ·			1RT ·	Willed
501.02z	2. #R106	SD1.02	A Addr.00z'	$\overline{}$	AAddr.00'	CAddr.00z	2· #R25 ₁	CAddr.00'
	1RT	301.02		1RT '			1RT	
\$D1.03z	2. #R108	\$D1.03	RASAZ'	2· #R8 1	RASA'	RASCz	2· #R32 ₁	RASC'
	1RT ·		A A 44 - 00 - 1		AAddr.03		1RT '	
501.04z	2. #R98 ₁	SDI.04		1RT ·	AAddr.03	_ CAddr.03z*	2. #R29 ₁	CAddr.03
	1RT .		A Add r. O6z	# B2	AAddr 06'	CAddr.06z*	# 556	C444-001
<u>\$01.05z</u>	2. #R109	SD1.05		1RT ·	200.00	CAUC1.002	1RT .	CACCI.U6
ED 1.06*	2. #R99 ₁	001.00	AAddr.02z	2· #R3 1	AAddr.02'	CAddr.02z*	2· #R27 ₁	CAddr.021
501.06z	1RT .	\$D1.06		iRT '			1RT ·	
\$01.07z	2. #R107	SD1.07	AAddr.04z		AAddr.04	CAddr.04z	2· #R31 ₁	CAddr.04'
	1RT ·			1RT '			1RT '	
\$D1.08z	2. #R112	SDI.08	AAddr.01z'	2· #R9 1	AAddr.01'	CAddr 01z	2. #R34 ₁	CAddr.01
	1RT		AAddr.05z`	# 04	A A = = 0 = :	24	1RT '	_
\$D1.09z	2· #R111	SD1.09		1RT '	AAddr.05	CAddr.05z	2. #R28 ₁	CAddr.05°
	1RT '		AAddr.07z	2· #R6 1	AAddr.07°	CAddr.07z	# 020	CAddr.07
_ \$ D1.10z	2. #R110 1RT	SDI.10		1RT '			1RT ·	
\$DI.11z	2. #R100	SDI.11	CACRE	a. #R23.			# 847	
201.112	1RT .	301.11	CASBz`	2· #R23 ₁	CASB.	CASDz	2 #R47 ₁	CASD'
_ \$DI.12z	2. #R107	SDI.12	WriteBz	2. #R21 ₁	WriteB'	Write Dz '	2. #R46 ₁	WriteD'
	1RT ·			1RT ·			1RT	WriteD
\$DI.13z	2. #R102	SDI.13	BAddr.00z	# 012	BAddr.00'	DAddr.00z'	2. #R371	v Dåddr.00*
	1RT '			1RT			1RT '	
\$DI.14z	2. #R103 1RT	SDI.14	RASBz	2· #R20 ₁	RASB.	RASDz	2· #R43 ₁	RASD'
\$ DI.15z		501.45		1RT '			1RT	
301.132	1RT .	SDI.15	BAddr 03z	2· #R17 ₁	BAddr.03	DAddr.03z	2. #R41, 1	DAddr.03'
\$DI.Parz	2. #R128	SDI.Pai	BAddr 06z		BAddr 06°	DAddr.06z`	1 ==	24
	1RT			2· #R14 ₁	5400100		1RT ·	DAddr.06*
vcc	2. #R132 P	Pullup A	BAddr 02z	2· #R15 ₁	BAddr.02	DAddr.02z	2. #R391	DAddr.02'
¥00				1RT :			1RT ·	,
<u>vcc</u>	2· #R129 p	, allab B	BACCT 04z	2. #R19 ₁	BAddr.04'	DAddr.04z'	2· #R44 ₁	DAddr.04'
vcc	2. #R12 _{1 Re}	efresh A'		1RT .			1RT '	
	1RT '		BAcc C:	2 #R22 ₁	BAddr.01	DAddr.01z	V V V	DAddr.01'
			Etaa		0.444-065		1RT .	
				2 #R16 ₁	BACCT.US	DAddr.05z`	1RT	DAddr.05
			BAcc+ 17z	2. #R18 ₁	BAcdr.07	DAddr.07z		DV44102;
R∈f <u>reshBz'</u>	2. #R24 1 Refr	eshB'		2 #R18 ₁			1RT .	UA001.07
	*RT '			000				
R+t <u>reshCz</u>	2 # R36 1 Refri	eshC'	RefreshEz 2	R60 1 Refresh	Ε.	RefreshGz 2 #		ūG,
Refreshno	2 # R48 1 Refr	achD'					RT '	
resilve	1RT '	<u>esn</u> u	RetreshFz 2 #	RT Refresh	F.	RefreshHz' 2' #1	1 Refresi	āH,
					D	iscrete Resist	ors	

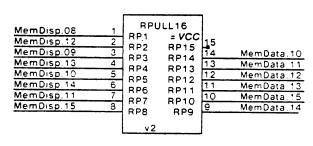
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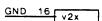
CASEZ	2·#R59 1	CASE	CASGz ·	2· #R83 1	CASG'
	1RT ·			1RT ·	
WriteEz*	2· #R58 1	WriteE'	WriteGz'	2. #R84 1	WriteG'
	1RT '			1RT ·	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,
EAddr.00z'	2. #R49 1	EAddr.00'	GAddr.00z	2·#R73 1	GAddr.00°
	1RT ·			1RT ·	
RASEz	2· #R56 1	RASE	RASGz	2· #R79 1	RASG.
	1RT		 	1RT	
EAddr.01z	2·#R57	EAddr.01	GAddr.01z	2 # R82 1	GAddr.01
	1RT ·			1RT ·	
EAddr.02z'	2. #R51 1	EAddr.02	GAddr.02z	2 # R75 1	GAddr.02
	1RT ·			1RT .	
EAddr.03z	2. #R53 1	EAddr 03	GAddr.03z'	2·#R77 1	GAddr.03
	1RT ·			1RT ·	
EAddr.04z	2. #R55 1	EAddr.04	GAddr.04z	2. #R81	GAddr.04'
	1RT .			1RT ·	
EAddr.05z	2. # R52 1	EAddr 05	GAddr.05z	2· #R76 1	GAddr.05
	1RT ·			1RT ·	
EAddr.06z	2. #R50 1	EAddr.06	GAddr.06z	2 [.] #R74 1	GAddr.06'
	1RT ·			1RT ·	
_EAddr.07z1	2·#R54 1	EAddr.07	GAddr.07z	2· #R78 1	GAddr.07'
	1RT ·		,	1RT .	
	# 50.4				
CASFz'	2· #R64 1	CASF'	CASHz'	2· #R86 1	CASH.
	1RT		CASHz'	1RT ·	
CASFz' WriteFz'	1RT ·		CASHz' WriteHz'	1RT ·	
	1RT ·	WriteF'		1RT ·	WriteH'
	1RT . 2· #R71 1 1RT . 2· #R61 1	WriteF'		1RT ·	WriteH'
WriteFz'	1RT · 2· #R71 1 1RT · 2· #R61 1 1RT ·	WriteF'	WriteHz'	1RT · · · · · · · · · · · · · · · · · · ·	WriteH'
WriteFz'	1RT . 2· #R71 1 1RT . 2· #R61 1	WriteF'	WriteHz'	1RT ·	WriteH'
WriteFz' FAddr.00z' RASFz'	1RT	WriteF'	WriteHz' HAddr.00z'	1RT ·	WriteH' HAddr.00'
WriteFz' FAddr.00z'	1RT	WriteF'	WriteHz' HAddr.00z'	1RT ·	WriteH' HAddr.00'
WriteFz' FAddr.00z' RASFz' FAddr.01z'	1RT	WriteF' FAddr.00' RASF' FAddr.01'	WriteHz' HAddr.00z' RASHz'	1RT 2 #R90 1 1RT 2 #R80 1 1RT 2 #R94 1 1RT	WriteH HAddr.00 RASH HAddr.01
WriteFz' FAddr.00z' RASFz' FAddr.01z'	1RT	WriteF' FAddr.00' RASF' FAddr.01'	WriteHz' HAddr.00z' RASHz'	1RT 2 #R90 1 1RT 2 #R80 1 1RT 2 #R94 1 1RT	WriteH' HAddr.00'
WriteFz' FAddr.00z' RASFz' FAddr.01z' FAddr.02z'	1RT	WriteF' FAddr.00' RASF' FAddr.01' FAddr.02'	WriteHz' HAddr.00z' RASHz' HAddr.01z'	1RT	WriteH HAddr.00 RASH: HAddr.01' HAddr.02
WriteFz' FAddr.00z' RASFz' FAddr.01z' FAddr.02z'	1RT	WriteF' FAddr.00' RASF' FAddr.01' FAddr.02'	WriteHz' HAddr.00z' RASHz' HAddr.01z'	1RT	WriteH HAddr.00 RASH: HAddr.01' HAddr.02
WriteFz' FAddr.00z' RASFz' FAddr.01z' FAddr.02z' FAddr.03z'	1RT	WriteF' FAddr.00' RASF' FAddr.01' FAddr.02'	WriteHz' HAddr.00z' RASHz' HAddr.01z' HAddr.02z'	1RT ·	WriteH HAddr.00 RASH HAddr.01
WriteFz' FAddr.00z' RASFz' FAddr.01z' FAddr.02z' FAddr.03z'	1RT	WriteF' FAddr.00' RASF' FAddr.01' FAddr.02'	WriteHz' HAddr.00z' RASHz' HAddr.01z' HAddr.02z'	1RT 2 #R87 1 1RT 2 #R80 1 1RT 2 #R94 1 1RT 2 #R91 1 1RT 2 #R93 1 1RT	WriteH HAddr.00 RASH: HAddr.01' HAddr.02
WriteFz' FAddr.00z' RASFz' FAddr.01z' FAddr.02z' FAddr.03z'	1RT	WriteF' FAddr.00' RASF' FAddr.01' FAddr.02'	WriteHz' HAddr.00z' RASHz' HAddr.01z' HAddr.02z'	1RT 2 #R87 1 1RT 2 #R80 1 1RT 2 #R94 1 1RT 2 #R91 1 1RT 2 #R93 1 1RT	WriteH HAddr.00 RASH HAddr.01 HAddr.02
WriteFz' FAddr.00z' RASFz' FAddr.01z' FAddr.02z' FAddr.03z'	1RT	WriteF' FAddr.00' RASF' FAddr.01' FAddr.02'	WriteHz' HAddr.00z' RASHz' HAddr.01z' HAddr.02z'	1RT	WriteH HAddr.00 RASH: HAddr.01 HAddr.02 HAddr.03
FAddr.00z' FAddr.01z' FAddr.03z' FAddr.04z' FAddr.05z'	1RT	WriteF' FAddr.00' RASF' FAddr.01' FAddr.02' FAcdr.03' FAcdr.03'	HADDI.OOZ' HADDI.OOZ' HADDI.OOZ' HADDI.OOZ'	1RT	WriteH HAddr.00 RASH HAddr.01 HAddr.02
FAddr.00z' FAddr.01z' FAddr.03z' FAddr.04z' FAddr.05z'	1RT	WriteF' FAddr.00' RASF' FAddr.01' FAddr.02' FAcdr.03' FAcdr.03'	HADDI.OOZ' HADDI.OOZ' HADDI.OOZ' HADDI.OOZ'	1RT	WriteH HAddr.00 RASH: HAddr.01 HAddr.02 HAddr.03 HAddr.03
FAddr.00z' FAddr.01z' FAddr.03z' FAddr.04z' FAddr.05z'	1RT	WriteF' FAddr.00' RASF' FAddr.01' FAddr.02' FAcdr.03' FAcdr.03'	HADDI.002' HADDI.002' HADDI.002' HADDI.002' HADDI.002'	1RT	WriteH HAddr.00 RASH: HAddr.01 HAddr.02 HAddr.03
FAddr.00z' FAddr.01z' FAddr.03z' FAddr.04z' FAddr.05z' FAddr.05z'	1RT 2: #R71 1RT 2: #R61 1RT 2: #R61 1RT 2: #R69 1RT 2: #R63 1RT 2: #R65 1RT 2: #R65 1RT 2: #R68 1 1RT 2: #R68 1 1RT 2: #R68	WriteF' FAddr.00' RASF' FAddr.01' FAddr.02' FAcdr.03' FAcdr.04' FAddr.05'	HADDI.002' HADDI.002' HADDI.002' HADDI.002' HADDI.002'	1RT 2 #R87 1 1RT 2 #R90 1 1RT 2 #R94 1 1RT 2 #R93 1 1RT 2 #R95 1 1RT 2 #R92 1 1RT 2 #R89 1 1RT 2 #R89 1 1RT 2 #R89 1 1RT	WriteH HAddr.00 RASH: HAddr.01 HAddr.02 HAddr.03 HAddr.04 HAddr.05
FAddr.00z' FAddr.01z' FAddr.03z' FAddr.04z' FAddr.05z' FAddr.05z'	1RT 2 # R71 1RT 2 # R61 1RT 2 # R70 1RT 2 # R69 1 RT 2 # R63 1 RT 2 # R65 1 RT 2 # R65 1 RT 2 # R68	WriteF' FAddr.00' RASF' FAddr.01' FAddr.02' FAcdr.03' FAcdr.04' FAddr.05'	HAddr.00z' HAddr.01z' HAddr.02z' HAddr.03z' HAddr.05z' HAddr.06z'	1RT 2 #R87 1 1RT 2 #R90 1 1RT 2 #R94 1 1RT 2 #R93 1 1RT 2 #R95 1 1RT 2 #R92 1 1RT 2 #R89 1 1RT 2 #R89 1 1RT 2 #R89 1 1RT	WriteH HAddr.00 RASH: HAddr.01 HAddr.02 HAddr.03 HAddr.03

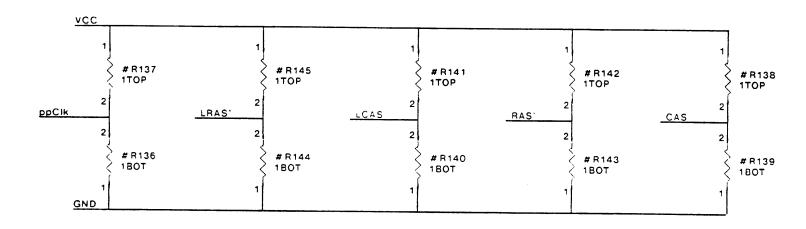
Discrete Resistors

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	(EROX	Project		File	Designer	Rev	Date	Page
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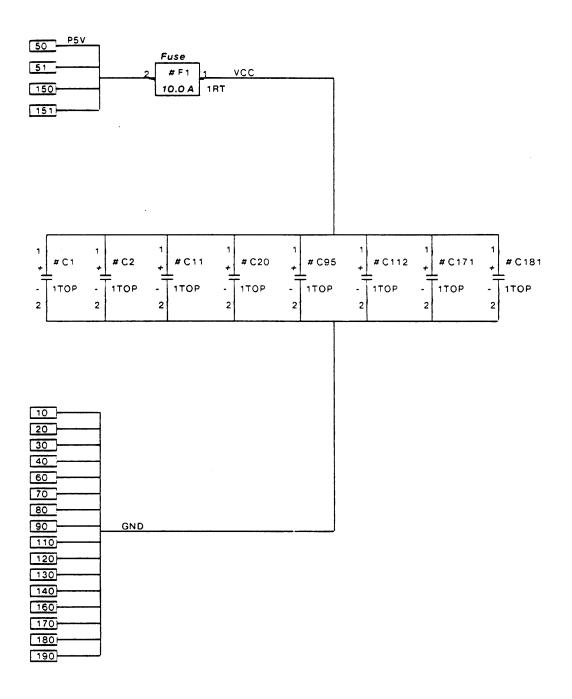






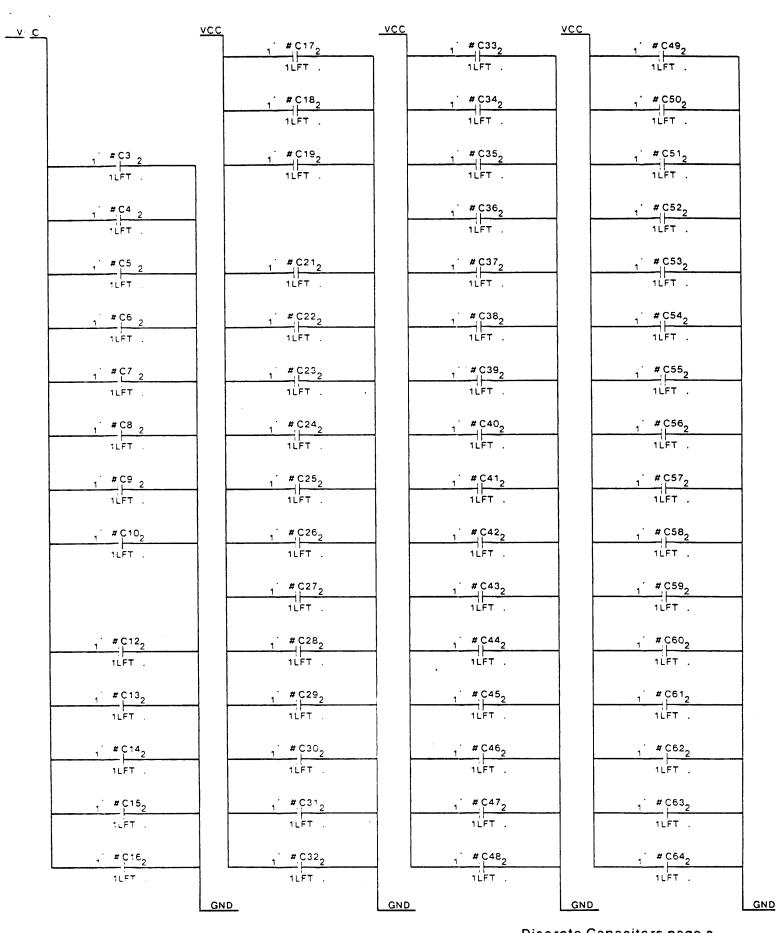
Resistor DIPs, Termination

XEROX	Project		File	Designer	Rev	Date	Page
: ED	MCCP	Resistor DIPs. Termination	PMCCP33.sil	S. Ando	Α	8/31/83	33



Power Pins - Fuses - Caps

KEROX	Project		File	Designer	Rev	Date	Page
ED	MCCP	Power Pins - Fuses - Caps	PMCCP34.sil	S. Ando	Α	10/17/83	34



Discrete Capacitors page a

Project MCCP Discrete Capacitors Pfile Designer Rev Date Page
PMCCP35.sil S. Ando A 8/31/83 35

<u>cc</u>	<u>,,cc</u>	<u>vcc</u> <u>v</u>	<u>cc</u>
#C65 ₂	1 # C81 ₂	1 #C97 ₂	1 #C113 1LFT
1LFT .	1LFT	1LFT .	1LFT .
, #C66 ₂	, #C82 ₂	1 # C98 ₂	1 #C114
1 # C66 ₂	1 #C82 ₂	1 #C98 ₂	1 #C114 1LFT .
# C67 ₂	# C83 ₂	1 # C99 ₂	, #C115
1 # C67 ₂	1 #C83 ₂	1 #C99 ₂	1 #C115 1LFT .
, #C68 ₂	, #C84 ₂	, #C10Q	#C116
1 # C68 ₂	1 #C84 ₂	1 #C10 <u>9</u>	1 # C116 1 LFT
, #C69 ₃	, #C85 ₂	, #C104	, #C117
1 # C69 ₂	1 #C85 ₂	1 #C10 <u>1</u>	1 #C117 1 1 1LFT
#C70a	, #C86 ₃	, #C102	, #C118
1 # C70 ₂	1 #C86 ₂	1 #C102 1LFT	1 # C118 1 LFT
, #C71	#C87	, #C103	, #C119
1 # C71 ₂	1 # C87 ₂	1 #C103 1LFT .	1 # C119 1 LFT
#C72 ₂	, #C88 ₂	, #C104	, #C12Q
, #C72 ₂	1 #C88 ₂	1 #C10g	1 #C120 1 LFT .
, #C73 ₃	, #C89 ₂	# C105	, #C124
1 # C73 ₂	1 #C89 ₂	1 #C10 <u>5</u>	1 #C123 1LFT
, #C74 ₂	, #C90 ₂	, #C106	, #C122
1 # C74 ₂	1 # C90 ₂	1 #C10 <u>5</u>	1 #C122 1 TLFT
, #C75 ₃	, #C91 ₂	, #C107	. #C123
1 # C75 ₂	1 #C91 ₂	1 #C10Z	1 #C123
, #C76 ₂	1 #C92 ₂	, #.C108	, #C124
1 # C76 ₂	1LFT .	1 #C108 1 1LFT .	1 #C124 1LFT
1 #C77 ₂	1 #C93 ₂	1 #C109	, #C125
1LFT .	1LFT	1LFT .	1LFT .
#C78 ₂	1 #C94 ₂	, #C11 <u>0</u>	# C125
1LFT .	1LFT	1LFT .	1LFT
#C79 ₂		1 #C112	# C127
1LFT .		1LFT .	1LFT .
, #C80 ₂	# C96 ₂		1 # C128
1LFT .	1 #C96 ₂		1LFT

Discrete Capacitors page b

Γ	{EROX	Project		File	Designer	Rev	Date	Page
١	ED	MCCP	Discrete Capacitors	PMCCP36.sil	S. Ando	Α	8/31/83	36
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# C129 # C146 # C152	_ <u>∢CC</u>		vcc	<u>vcc</u>		vcc	
# C13Q		#C129		5	1 #C163	1	1 #C177
# C133		1LFT .	1LFT		1LFT .		1LFT
# C133		, #C130	1 #C14	5	1 #C162		1 #C178
# C132 # C148 # C168 # C169 1		1LFT .	1LFT		1LFT .		1LFT .
# C132 # C148 # C168 # C169 1		, #C13 <u>1</u>	1 #C14	<u> </u>	1 #C163		1 # _{C179}
# C133		1LFT .	1LFT		1LFT .		1LFT .
# C133		#C132	1 #C14	8	, #C164		, #C180
# C13g		1LFT .	1LFT		1LFT .		1LFT
# C13g		, #C133	1 #C14	9	, #C165		
#C135		*LFT .	1LFT		1LFT .		
#C135		, #C13 4	, #C15	9	, #C165		, #C182
#C136		1LFT .	1LFT	.	1LFT .		1LFT
#C136		, #C135	#C15	4	, #C167		, #C183
#C132		1LFT	1LFT	·	1LFT .		1LFT .
#C132		, #C13 <u>6</u>	, #C15	2	, #C168		_ #C184
#C138		1LFT .	1LFT		1LFT .		1LFT .
#C138		_`#C137	, #C15	3	, #C169		# C185
1 #C139 1 #C152 1 #C152 1 #C172 1 #C182 1 #C182 1 #C182 1 #C182 1		1LFT .	1LFT		1LFT .		1LFT .
1 #C139 1 #C152 1 #C152 1 #C172 1 #C182 1 #C182 1 #C182 1 #C182 1		# C138	, #C15	4	,' #C170		#C186
1 # C156 1 # C172 1 LFT		1LFT .	1LFT		1 2 1 1 1 1 1 1 1 1		1LFT .
1 # C156 1 # C172 1 LFT		_ #C139	_ #C15	5			. #C187
1 LFT . 1 LFT		1LFT .	1LFT				1LFT .
1 LFT . 1 LFT		. #C140	#C15	.6	.* #C172		* #C188
1 # C142 1 # C158 1 # C174 1 LFT . 1 L					1 Z 1LFT .		
1 # C142 1 # C158 1 # C174 1 LFT . 1 L		# C141	. #C15	.7	# C173		# C189
1 #C143 1 #C159 1 LFT . 1LFT . 1LFT . 1LFT . 1LFT . 1LFT .			1LFT		1 2 2 1 1 1 1 1 1 1		
1 #C143 1 #C159 1 LFT . 1LFT . 1LFT . 1LFT . 1LFT . 1LFT .		# C142	_ #C15	8	_`#C174		
1 #C144 1 #C169 1 #C175 1LFT .		1 2 1LFT ,	1LFT		1 FT .		
1 #C144 1 #C169 1 #C175 1LFT .		_`#C143	, #C15	9	, #C175		
1 #C144 1 #C160 1 #C175 1LFT .		1LFT	1 1LFT		1 LFT .		
1LFT . 1LFT .		.`#C14 #	_ #C16	1 1	, #C176		
GND GND GND					1LFT .		
			GND	GND		GND	GND

Discrete Capacitors page c

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ı	XEROX	Project	•	File	Designer	Rev	Date	Page
١	ED	MCCP	Discrete Capacitors	PMCCP37.sil	S. Ando	Α	10/17/83	37
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